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	IEE Conference	Γ	1. A 145 MHz user-programmable gate array
IEEE	Proceeding IEEE Standard		do Valle Simoes, E.; Barone, D.A.C.; Rapid System Prototyping, 1995. Proceedings., Sixth IEEE International Workshop on 7-9 June 1995 Page(s):226 - 232
STD	ILLE Standard		AbstractPlus Full Text: PDF(588 KB) IEEE CNF
		Γ	2. On the re-quantization of data to implement high-order narrow-band filters using logic Dick, C.H.; Harris, F.; Signals, Systems and Computers, 1995. 1995 Conference Record of the Twenty-Ninth
			Conference on Volume 2, 30 Oct2 Nov. 1995 Page(s):1347 - 1351 vol.2
	•		AbstractPlus Full Text: PDF(428 KB) IEEE CNF
		Γ	 Design of a systolic coprocessor for rational addition Jebelean, T.; Application Specific Array Processors, 1995. Proceedings., International Conference of 24-26 July 1995 Page(s):282 - 289
			AbstractPlus Full Text: PDF(344 KB) IEEE CNF
		Г	4. An FPGA-based point pattern matching processor with application to fingerprint Ratha, N.K.; Jain, A.K.; Rover, D.T.; Computer Architectures for Machine Perception, 1995. Proceedings. CAMP '95 18-20 Sept. 1995 Page(s):
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		Γ	5. An automated design process for the CHAMP module Patriquin, R.; Gurevich, I.; Aerospace and Electronics Conference, 1995. NAECON 1995., Proceedings of the IEE Volume 1, 22-26 May 1995 Page(s):417 - 424 vol.1
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		Γ	6. A dedicated digital unit for event recognition and centroiding in photon counting CCDs Achdjian, P.; D'Angelo, S.; Quadrini, E.; Sechi, G.R.; Tanzi, E.G.; Real-Time Systems, 1995. Proceedings., Seventh Euromicro Workshop on

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7. Flexible IF processors for future communications payloads

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8. Time-area efficient multiplier-free filter architectures for FPGA implementation

Shajaan, M.; Nielsen, K.; Sorensen, J.A.;

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Narasimhan, D.; Fernandes, D.; Raj, V.K.; Dorenbosch, J.; Bowden, M.; Kapoor, V.S.; Custom Integrated Circuits Conference, 1993., Proceedings of the IEEE 1993 9-12 May 1993 Page(s):3.1.1 - 3.1.4

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L1	7	(US-20040088150-\$).did. or (US-6188975-\$ or US-5870588-\$ or US-5937185-\$ or US-3891974-\$ or US-5594890-\$ or US-6057706-\$).did.	US-PGPUB; USPAT	OR	OFF	2005/06/13 11:35
S1	12	(hardware with software with (co-verification coverification)).ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 11:22
S3	3	("6321366" "6810442" "6754763").pn.	USPAT	OR	OFF	2005/06/03 10:45
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S 9	149	(EDA) and DMA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 11:05
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S14	8	("5663900" "6161199" "6347395" "6009270" "6185522" "6223144" "6173419" "6516428").pn.	USPAT	OR	OFF	2005/06/03 11:44

S15	5	("6202044" "5748875" "5978584" "6356862" "6718294").pn.	USPAT	OR	OFF	2005/06/03 11:48
S16	3	("5748875" "6202044" "5493723").pn.	USPAT	OR	OFF	2005/06/03 11:49
S17	0	(EDA) same ((internal with bus) with (PLA PLD FPGA MCM))	USPAT	OR	OFF	2005/06/06 15:04
S18	27	(EDA) with (PLA PLD FPGA MCM)	USPAT	OR	OFF	2005/06/03 11:58
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S20	0	"09/954715"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 11:58
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S22	0	EDA same PCI same DMA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 12:00
S23	14	EDA same PCI	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 12:00
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S25	2	"6009256".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 12:26

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		"5659716" "5661409" "5661662" "5761097" "5796623" "5841967" "5867541" "6009256" "6009531").PN.				
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S28	11	S27 and (eda and model and bus (reconfig\$ FPGA PLD) and memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR .	OFF	2005/06/03 14:34
S29	1	S28 and DMA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 14:21
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S 31	1	S28 and (shared adj memory)	USPAT; DERWENT	OR	OFF	2005/06/03 14:30
S32	2	S27 and (eda and model and bus and (reconfig\$ FPGA PLD) and memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 14:34
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S35	1	(behavioral with (RTL gate)).ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 14:45

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S36	32	(behavioral with synthesi\$6).ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 14:50
S37	0	"7564951".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 14:50
S38	2	"5764951".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; .IBM_TDB	OR	OFF	2005/06/03 14:50
S39	8	("4827427" "5111413" "5128871" "5237513" "5274793" "5437037" "5544066" "5572437").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/03 14:50
S40	31	("4353117" "4587625" "4635208" "4675832" "4697241" "4703435" "4789944" "4805113" "4813013" "4827427" "4831543" "4833619" "4890238" "4908772" "4918614" "4922432" "4965741" "4967367" "4970664" "5005136" "5034899" "5084824" "5111413" "5164908" "5164911" "5220512" "5222030" "T940008" "T940020").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/03 14:51
S41	10	eda and ((shar\$3 adj3 memory) with (co-simulation cosimulation co-emulation coemulation coverification co-verification))	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/03 14:54
S42	10	eda and (((shar\$3 common central host) adj3 memory) with (co-simulation cosimulation co-emulation coemulation coverification co-verification))	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/03 14:54
S43	10	eda and (((shar\$3 common central target) adj3 memory) with (co-simulation cosimulation co-emulation coemulation coverification co-verification))	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/03 14:55
S44	0	(hardware with design with tool) and (((shar\$3 common central target) adj3 memory) with (co-simulation cosimulation co-emulation coemulation coverification co-verification))	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/03 14:56
S45	10	(design with tool) and (((shar\$3 common central target) adj3 memory) with (co-simulation cosimulation co-emulation coemulation coverification co-verification))	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/03 14:56
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S48	75	eda and control\$4 and DMA	USPAT; DERWENT	OR	OFF	2005/06/06 11:36
S49	3	("5815688" "5838948" "6134516").PN.	US-PGPUB; USPAT; USOCR	OR .	OFF	2005/06/03 15:50
S50	31	("4835736" "5058114" "55568437" "5572712" "5640542" "5717699" "5764079" "5821771" "5870410" "5960191" "6014334" "6016563" "6020758" "6157210" "6182247").PN. OR ("6286114").URPN.	US-PGPUB; USPAT; USOCR	OR .	OFF	2005/06/03 16:40
S51	0	(FPGA PLD (reconfigurable adj logic)) same memory same (hardware with software) same (cosimualtion co-simulat\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 16:52

S52	0	(FPGA PLD PLA) same memory same (hardware with software) same (cosimualtion co-simulat\$6)	US-PGPUB; USPAT;	OR	OFF	2005/06/03 16:53
	1		USOCR; EPO; JPO; DERWENT; IBM_TDB			·
S53	1124	(FPGA PLD PLA) same memory same (hardware with software)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 16:53
S54	155	(FPGA PLD PLA) with memory with (hardware with software)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/03 16:53
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S65	76	(bus) and (shared with memory) and (eda (electronic with design with automation))	USPAT; DERWENT	OR	OFF	2005/06/06 14:24
S66	8	S63 and (state with machine)	USPAT; DERWENT	OR	OFF	2005/06/06 14:44
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S73	840	(DMA with PCI) and (emulat\$4 simulat\$4 model\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	OR	OFF	2005/06/06 16:42
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S 83	1	"5450551".pn.	USPAT	OR	OFF	2005/06/06 19:53
S84	28	(US-5937179-\$ or US-5684721-\$ or US-5329471-\$ or US-6691268-\$ or US-6836877-\$ or US-5546562-\$ or US-6356862-\$ or US-6223144-\$ or US-6188975-\$ or US-5663900-\$ or US-6202044-\$ or US-6286114-\$ or US-6182247-\$ or US-6212489-\$ or US-6052524-\$ or US-6075935-\$ or US-6209120-\$ or US-5572437-\$ or US-6298320-\$ or US-6108494-\$ or US-5838948-\$ or US-5815688-\$ or US-5968161-\$ or US-5661662-\$ or US-5970240-\$ or US-6094532-\$).did. or (US-6286128-\$ or EP-453171-\$).did.	USPAT; DERWENT	OR	OFF	2005/06/08 16:51
S85	9	S84 and (plural\$5 and reconfig\$8)	USPAT; DERWENT	OR	OFF	2005/06/07 14:48
S86	1105	FPGA with interconnect\$4	USPAT; DERWENT	OR	OFF	2005/06/07 15:06
S87	9	S86 and (hyper-cube hypercube)	USPAT; DERWENT	OR	OFF	2005/06/07 15:07
S88	136	series adj FPGA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 15:27
S89	0	(series adj interconnect\$4) with FPGA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 15:27
S 90	21	(series with interconnect\$4) with FPGA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 15:28
S91	22	daisy with chain with FPGA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 15:33
S92	165	703/20.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 17:45
593	1926	719/312,313,318,319,310.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 17:47

S94	4	S93 and unix and (verilog vhdl (hardware adj descript\$6 adj language))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 17:49
S95	10	S93 and (verilog vhdl (hardware adj descript\$6 adj language)) and (simulat\$4 emulat\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 17:52
S96		S93 and (verilog vhdl (hardware adj descript\$6 adj language))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 17:51
S97	330	S93 and (simulat\$4 emulat\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 18:13
S98	2	"5109353".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 18:13
S99	10	software with hardware with pointer with mapping	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:01
S10 0	1129	hardware with software with mapp\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:01
\$10 1	349	S100 and pointer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:01
S10 2	339	S101 not S99	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:04
S10 3	40	(control with logic) and ((data-in with pointer with logic) or (data-in with latch with logic))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:06
\$10 4	9	(control with logic) and ((data-in with pointer with logic))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:07

\$10 5	4	(pointer with based) same (hardware with software with mapping)	US-PGPUB; USPAT; USOCR;	OR	OFF	2005/06/08 11:08
			EPO; JPO; DERWENT; IBM_TDB			
S10 6	17	(pointer) same (hardware with software with mapping)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR ·	OFF	2005/06/08 16:50
S10 7	28	(US-5937179-\$ or US-5684721-\$ or US-5329471-\$ or US-6691268-\$ or US-6836877-\$ or US-5546562-\$ or US-6356862-\$ or US-6223144-\$ or US-6188975-\$ or US-5663900-\$ or US-6202044-\$ or US-6286114-\$ or US-6182247-\$ or US-6212489-\$ or US-6052524-\$ or US-6075935-\$ or US-6209120-\$ or US-5572437-\$ or US-6298320-\$ or US-6108494-\$ or US-5838948-\$ or US-5815688-\$ or US-5968161-\$ or US-5661662-\$ or US-5970240-\$ or US-6094532-\$).did. or (US-6286128-\$ or EP-453171-\$).did.	USPAT; DERWENT	OR	OFF	2005/06/08 16:51
S10 8	6	S107 and pointer	USPAT; DERWENT	OR	OFF	2005/06/08 16:54
S10 9	215	hardware with software with pointer	USPAT; DERWENT	OR	OFF	2005/06/08 16:54
S11 0	18	S109 with (simulat\$4 emulat\$4 model\$6)	USPAT; DERWENT	OR	OFF	2005/06/08 19:47
S11 1	1	"5867541".pn.	USPAT	OR	OFF	2005/06/08 19:53
S11 2	4	("5649176" "5659716" "5761097" "6009531").pn.	USPAT	OR	OFF	2005/06/08 19:54
S11 3	1072	delay with insensitive	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:18
S11 5	476	delay adj3 insensitive	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:18
S11 6	58	(delay adj3 insensitive) with asynchronous	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:23
S11 7	41	(timing with insensitive) same (trigger latch)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:25
S11 8	501	(timing with insensitive)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:25
S11 9	17	S118 and ((glitch adj2 free) or glitchless)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:28
S12 0	510	(glitch near free) and (trigger or latch or flip\$flop)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:28
S12 1	521	(glitch with free) and (trigger or latch or flip\$flop)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:30
S12 2	218	(glitch with free) same (trigger or latch or flip\$flop)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:46
S12 3	10	((gated or derived) near clock) same ((glitch adj less) or glitchfree or (glitch adj free) or glitchless)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 12:49

S12 4	9	FPGA same (external adj I/O) same simulat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:32
S12 6	9	reconfig\$6 same (external adj I/O) same simulat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:32
S12 7	9	(PLA PLD FPGA MCM) same (external adj I/O) same simulat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:33
S12 8	0	(PLA PLD FPGA MCM) same (external adj device) same simulat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:33
S12 9	0	FPGA same (external adj device) same simulat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:33
S13 0	0	FPGA same (external adj device) same (simulat\$4 emaulat\$4 model\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:33
\$13 1	9	FPGA same (external adj I/O) same (emaulat\$4 model\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:34
S13 2	48	FPGA same (external adj I/O)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:34
S13 3	39	S132 not S126	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:38
S13 5	138	(external with I/O) with (simulat\$4 emaulat\$4 model\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:39
\$13 7	129	S135 not S132	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:51

S13 8	1	read write multiple FPGA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	OFF	2005/06/09 15:52
\$13 9	0	(multiple with FPGA) same (access interface I/O read write)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	OFF	2005/06/09 15:52
S14 0	274	(multiple with FPGA) same (access interface I/O read write)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/09 15:52
S14 1	13	("5497498" "5535342" "5539330" "5583749" "5603043" "5628028" "5784636" "5788669" "5794062" "5991822" "5999990" "6185484" "6230307").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 16:04
S14 2	21	bus with selector with FPGA	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 16:10
S14 3	175	bus with selector and FPGA	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 17:54
S14 4	184	703/28.ccls.	US-PGPUB; USPAT; USOCR	OR:	OFF	2005/06/09 17:55
S14 5	331	703/21.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 17:55
S14 6	18	S145 and FPGA	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 17:57
S14 7	537	703/26,19.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 17:57
S14 8	65	S147 and (FPGA reconfig\$6 PLA PLD MCM)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/09 17:58
S15 0	40	(US-6169422-\$ or US-5387825-\$ or US-5808486-\$ or US-5539330-\$ or US-5109353-\$ or US-5329471-\$ or US-5363501-\$ or US-5546562-\$ or US-5572437-\$ or US-5661662-\$ or US-5663900-\$ or US-5684721-\$ or US-5815688-\$ or US-5838948-\$ or US-5905883-\$ or US-5911059-\$ or US-5937179-\$ or US-5968161-\$ or US-5970240-\$ or US-6052524-\$ or US-6075935-\$ or US-6094532-\$ or US-6108494-\$ or US-6182247-\$ or US-6188975-\$ or US-6202044-\$).did. or (US-6209120-\$ or US-6212489-\$ or US-6223144-\$ or US-6286114-\$ or US-6298320-\$ or US-636862-\$ or US-6691268-\$ or US-6766284-\$ or US-6368677-\$ or US-5535342-\$ or US-5603043-\$ or US-6304903-\$).did. or (EP-453171-\$ or US-6286128-\$).did.	USPAT; DERWENT	OR	OFF	2005/06/10 14:14

				T		
1	40	(US-6169422-\$ or US-5387825-\$ or US-5808486-\$ or US-5539330-\$ or US-5109353-\$ or US-5329471-\$ or US-5363501-\$ or US-5546562-\$ or US-5572437-\$ or US-5661662-\$ or US-5663900-\$ or US-5684721-\$ or US-5815688-\$ or US-5838948-\$ or US-5905883-\$ or US-5911059-\$ or US-5937179-\$ or US-5968161-\$ or US-5970240-\$ or US-6052524-\$ or US-6075935-\$ or US-6094532-\$ or US-6108494-\$ or US-6182247-\$ or US-6188975-\$ or US-6202044-\$).did. or (US-6209120-\$ or US-6212489-\$ or US-6223144-\$ or US-6286114-\$ or US-6298320-\$ or US-6356862-\$ or US-6691268-\$ or US-6766284-\$ or US-6304903-\$).did. or (EP-453171-\$ or US-6286128-\$).did.	USPAT; DERWENT	OR	OFF	2005/06/10 14:14
S15 2	21	S151 and (behavior\$3)	USPAT; DERWENT	OR	OFF	2005/06/10 14:40
S15 3	27	S151 and (condition\$4 if-then)	USPAT; DERWENT	OR	OFF	2005/06/10 14:51
\$15 4	27	S151 and (condition\$4 "if-then")	USPAT; DERWENT	OR	OFF	2005/06/10 14:52
S15 8	2	S151 and ("if-then"\$)	USPAT; DERWENT	OR	OFF	2005/06/10 14:53
S15 9	134	("if-then"\$) and (emulat\$4)	USPAT; DERWENT	OR	OFF	2005/06/10 17:01
S16 0	6594	((emulat\$4 hardware) with interrupt\$4) and ((software simulat\$4))	USPAT; DERWENT	OR	OFF	2005/06/10 17:02
S16 1	4885	((emulat\$4 hardware) with interrupt\$4) and ((software simulat\$4) with interrupt\$4)	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	OFF	2005/06/12 12:14
516 2	892	(emulat\$4 simulat\$4) and (hardware with interrupt\$5 with software)	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	ON	2005/06/12 13:54
S16 3	12	(emulat\$4 simulat\$4) and (hardware with initiated with interrupt\$5 with software)	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	ON	2005/06/12 12:15
S16 4	6	"if-then"\$ with interrupt\$6	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	ON	2005/06/12 15:12
\$16 5	3	"09/918600"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 12:44
S16 6	88	(PLD FPGA PLA reconfig\$9) with hardware with interrupt\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 12:44
S16 7	18	("3891974" "5274831" "5511217" "5884023" "5960191" "6055649" "6075941" "6085336" "6173386" "6289300" "6370606" "6385742" "6385747" "6446221" "6522985" "6564339" "6567933" "6681341").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:03

S16 8	2656	emulat\$ with (interrupt\$6 condition\$4)	US-PGPUB; USPAT;	OR	OFF	2005/06/12 13:07
S17	305	S168 and (FPGA PLA PLD)	USOCR US-PGPUB;	OR	OFF	2005/06/12 13:04
0	303		USPAT; USOCR	-		
S17 1	3	emulat\$ with (interrupt\$6) with ("test-bench" testbench)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:07
S17 2	3	emulat\$ with (interrupt\$6) with ("test-rig" simulator)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:10
S17 3	15	emulat\$ with (interrupt\$6) with (supend wait resume)	US-PGPUB; USPAT; USOCR	OR	OFF .	2005/06/12 13:10
S17 4	7	("5103394" "5666519" "5678028" "5737579" "5761477" "5815688" "6047381").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:11
\$17 5	9	("4638423" "5077657" "5459872" "5493672" "5546562" "5551013" "5572710" "5574927" "5600579").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:20
S17 6	12	(hardware with interrupt near software) same emulat\$4	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:20
S17 7	3	("5737516" "5828824" "6324684").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:29
S17 8	1442	FPGA and debug\$5	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:30
S17 9	176	FPGA with debug\$5	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 13:30
S18 0	68	(hardware with software with (co-verification coverification))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 13:37
S18 1	107	(hardware with software with (co-simulation cosimulation))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 13:37
S18 2	16	S180 and S181	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 13:37
S18 3	159	S180 or S181	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 13:37
S18 4	79	S183 and interrupt	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 13:38

S18 5	35	(emulat\$4 simulat\$4) and ((hardware with breakpoint\$5) near software)	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	ON	2005/06/12 13:54
S18 6	2	hardware near interrupt\$4 near service near software	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/12 14:42
S18 7	0	hardware adj interrupt\$4 adj service adj software	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/12 14:42
S18 8	279	hardware with interrupt\$4 with service with software	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/12 14:43
S18 9	0	("2004/0088150").URPN.	USPAT	OR	OFF	2005/06/12 14:45
\$19 0	47	(US-20040088150-\$).did. or (US-5109353-\$ or US-5329471-\$ or US-5363501-\$ or US-5387825-\$ or US-5535342-\$ or US-5539330-\$ or US-5546562-\$ or US-5572437-\$ or US-5603043-\$ or US-5661662-\$ or US-5663900-\$ or US-5684721-\$ or US-5808486-\$ or US-5815688-\$ or US-5838948-\$ or US-5905883-\$ or US-5911059-\$ or US-5937179-\$ or US-5968161-\$ or US-5970240-\$ or US-6052524-\$ or US-6075935-\$ or US-6094532-\$ or US-6108494-\$ or US-6169422-\$ or US-6182247-\$).did. or (US-6188975-\$ or US-6202044-\$ or US-6209120-\$ or US-6212489-\$ or US-6223144-\$ or US-6286114-\$ or US-6298320-\$ or US-6304903-\$ or US-6356862-\$ or US-6691268-\$ or US-6766284-\$ or US-6836877-\$ or US-5600579-\$ or US-5870588-\$ or US-5937185-\$ or US-3891974-\$ or US-5286128-\$).did. or (EP-453171-\$ or US-6286128-\$).did.	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/06/12 14:52
S19 1	26	\$190 and interrupt	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 15:01
S19 2	280	hardware with behavior with process\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 15:03
S19 3	112	S192 and emulat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/12 15:03
\$19 5	2	"if-then-else"\$ with interrupt\$6	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	ON	2005/06/12 15:13

S19 6	16	"if-then-else"\$ with hardware	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	ON	2005/06/12 15:15
S19 7	1	"if-then-else"\$ with emulat\$	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	ON	2005/06/12 15:15
S19 8	38	("4638423" "5077657" "5459872" "5493672" "5546562" "5551013" "5572710" "5574927" "5600579").PN. OR ("5838948").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 15:35
S19 9	1	"5815688".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 15:53
S20 0	513	(FPGA hardware) with implement\$ with interrupt	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/12 15:54
S20 1	41	buza.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/13 09:55
S20 2	7	bunza.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/13 09:55